하계종합학술대회 초청강연 발표정보

▌발표자 정보

성명	최해랑	사진
소속(학교)	SK 하이닉스 (Solution개발)	
부서(학과)	PIM HW	
직위	팀장	
발표분야	PIM, PIM 기반 가속 시스템	

약력	Haerang Choi is a team leader of PIM HW in SK hynix Inc. He received the B.S. and M.S. degrees from Hanyang University, Seoul, South Korea, in 2005 and 2007, respectively, and the Ph.D. degree in computer science and engineering from Seoul National University, Seoul, in 2021. From 2007 to 2016, he designs and develops dynamic random access memory (DRAM) circuits in SK hynix Inc. Since 2021, he has been developing Accelerator-in-Memory (AiM) and AiM-based accelerator architecture in SK hynix Inc. His current research interests include memory-centric system architectures for large language model and its accelerator.

▌초청강연 정보

제 목	On-device AI 향 DRAM 기반의 LLM 가속 시스템
Abstract	SK하이닉스에서 개발 중인 Accelerator-in-Memory (AiM)과 AiM 기반 가속 시스템 (AiMX) 소개